

FIG. 1

PRIOR ART

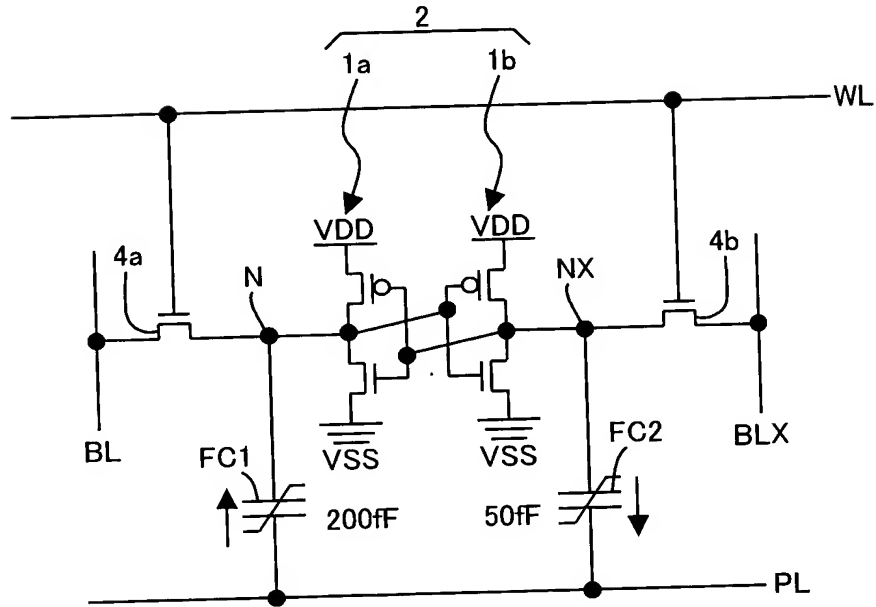
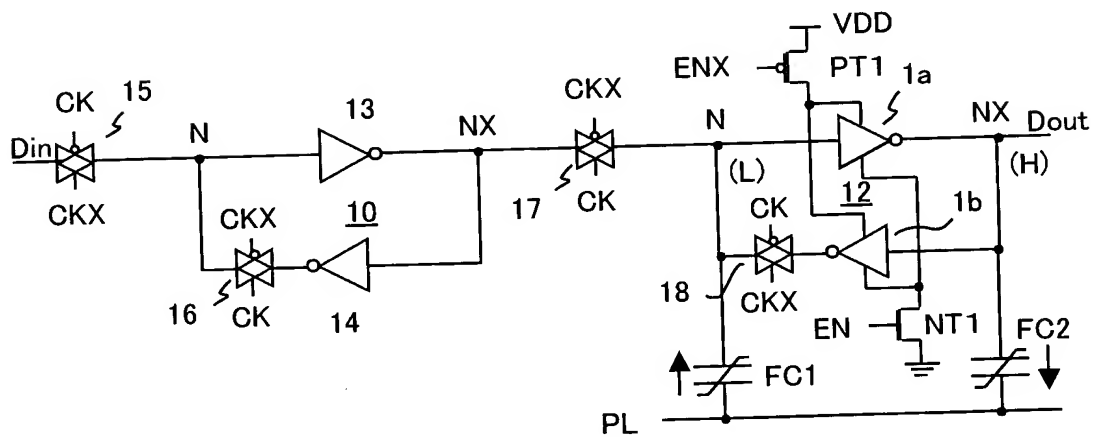


FIG. 2



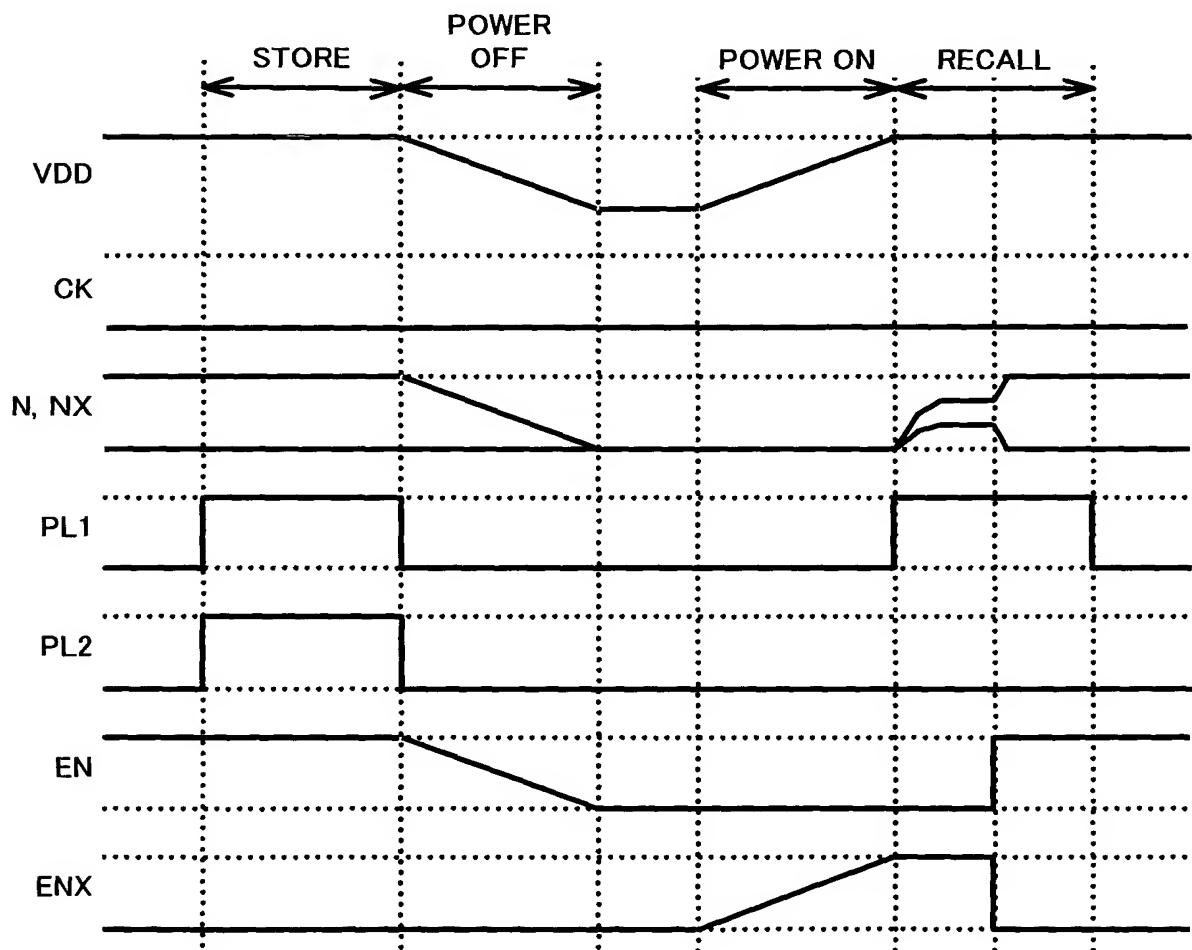


FIG. 5

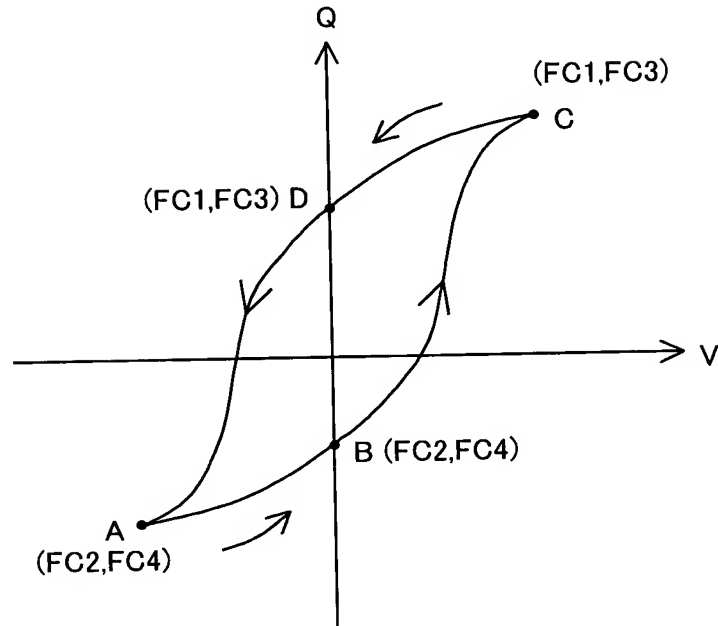


FIG. 6

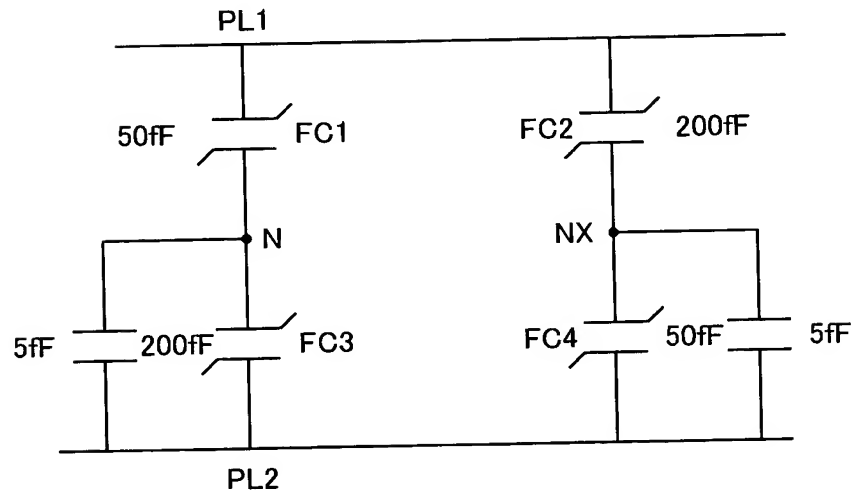


FIG. 7A

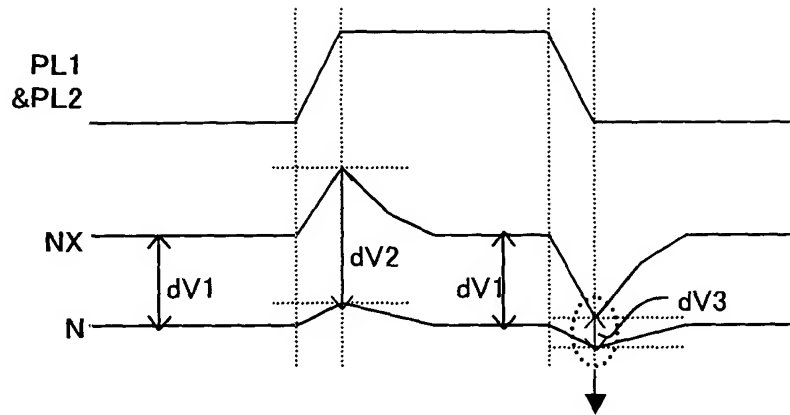


FIG. 7B

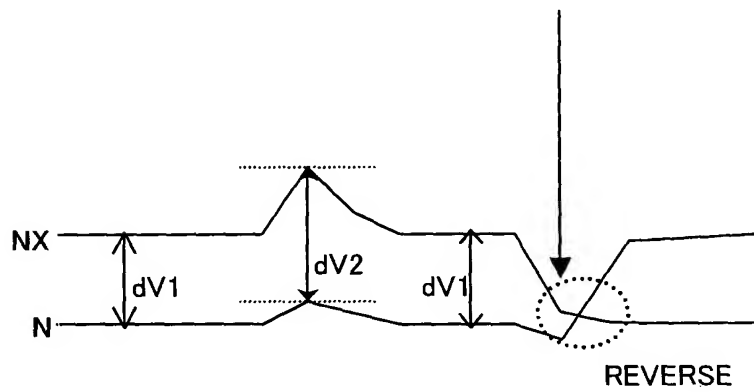


FIG. 8

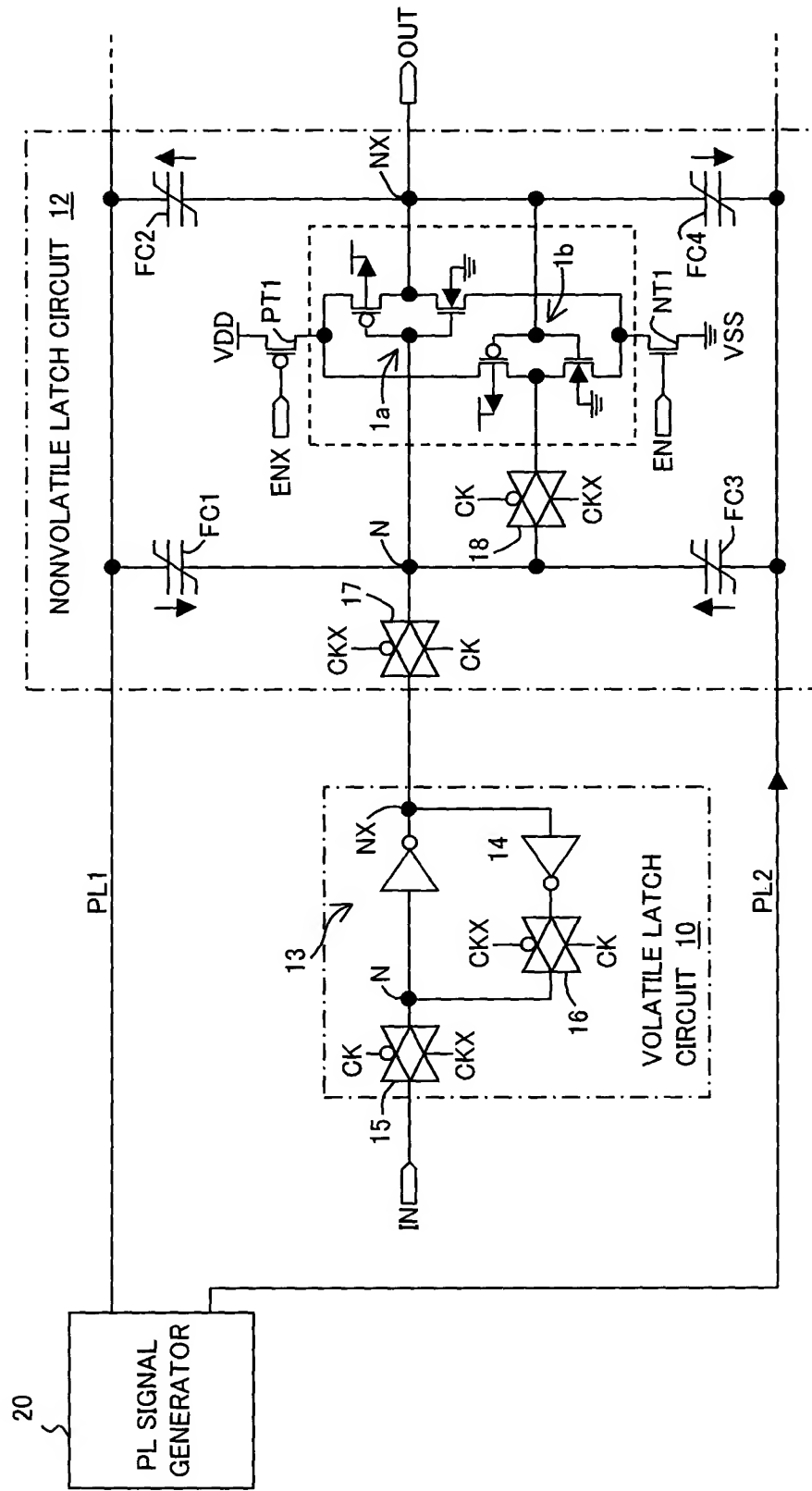
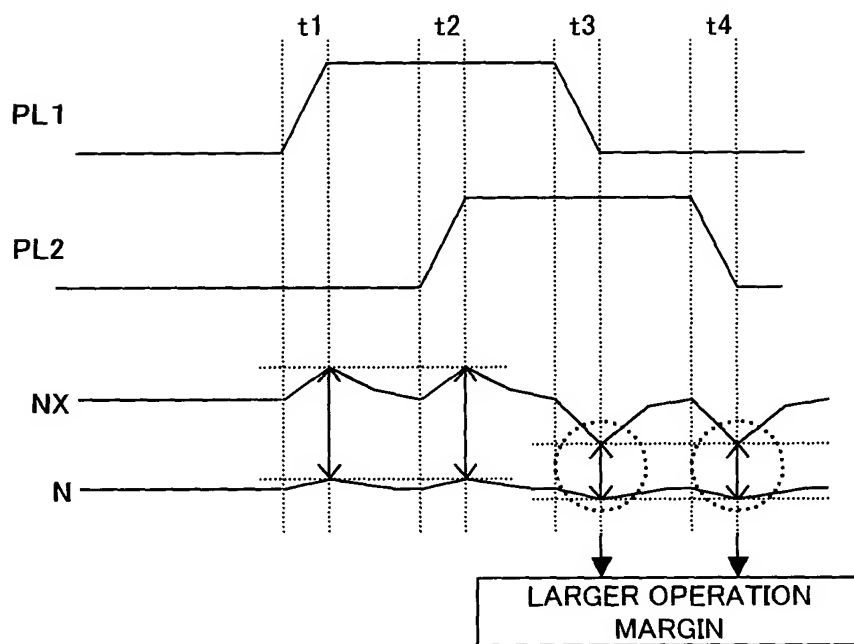


FIG. 9



FIRST EXAMPLE

FIG. 10

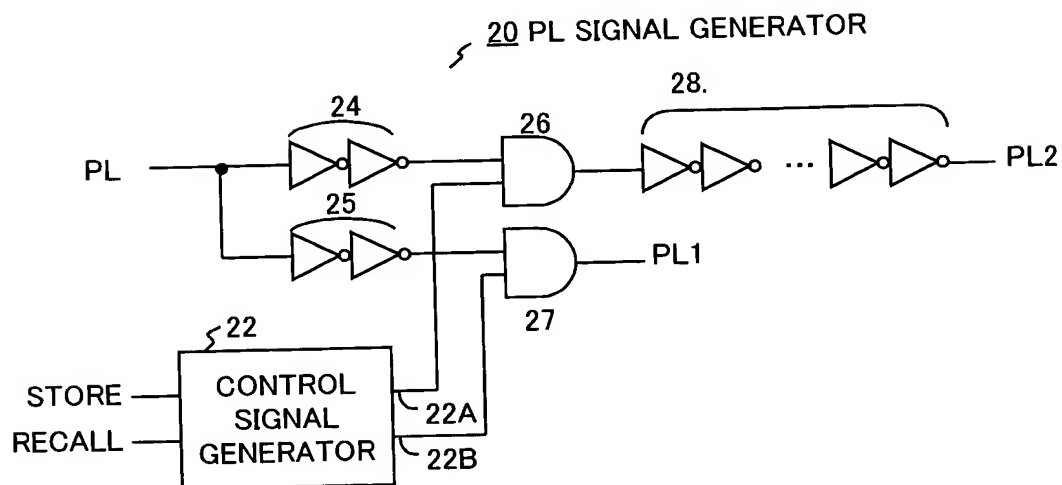


FIG. 11

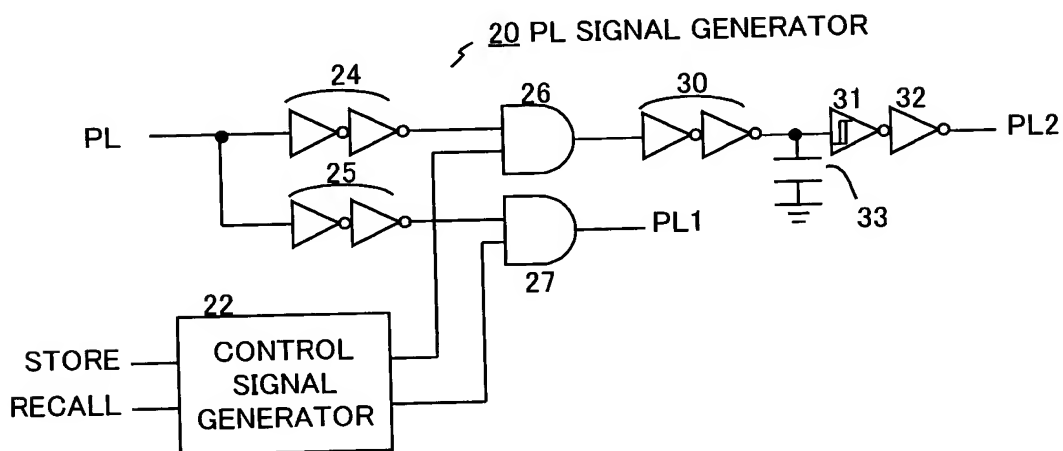
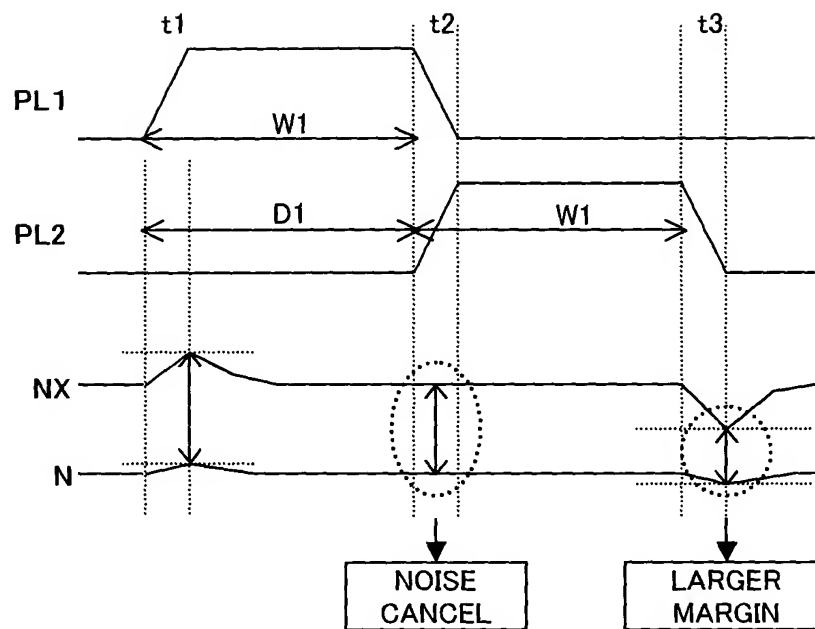


FIG. 12



SECOND EXAMPLE

FIG. 13

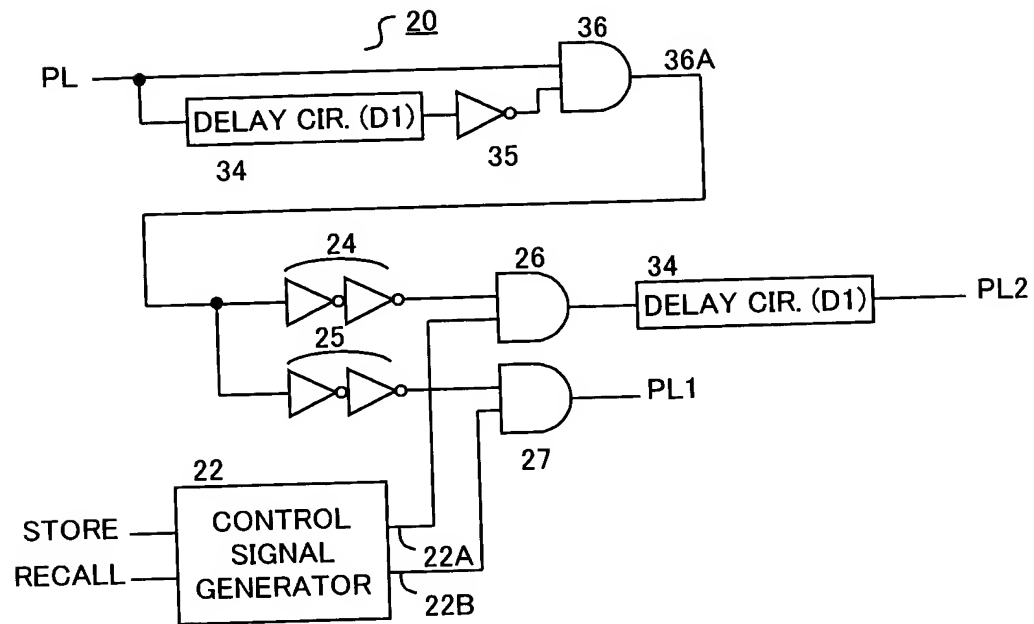


FIG. 14

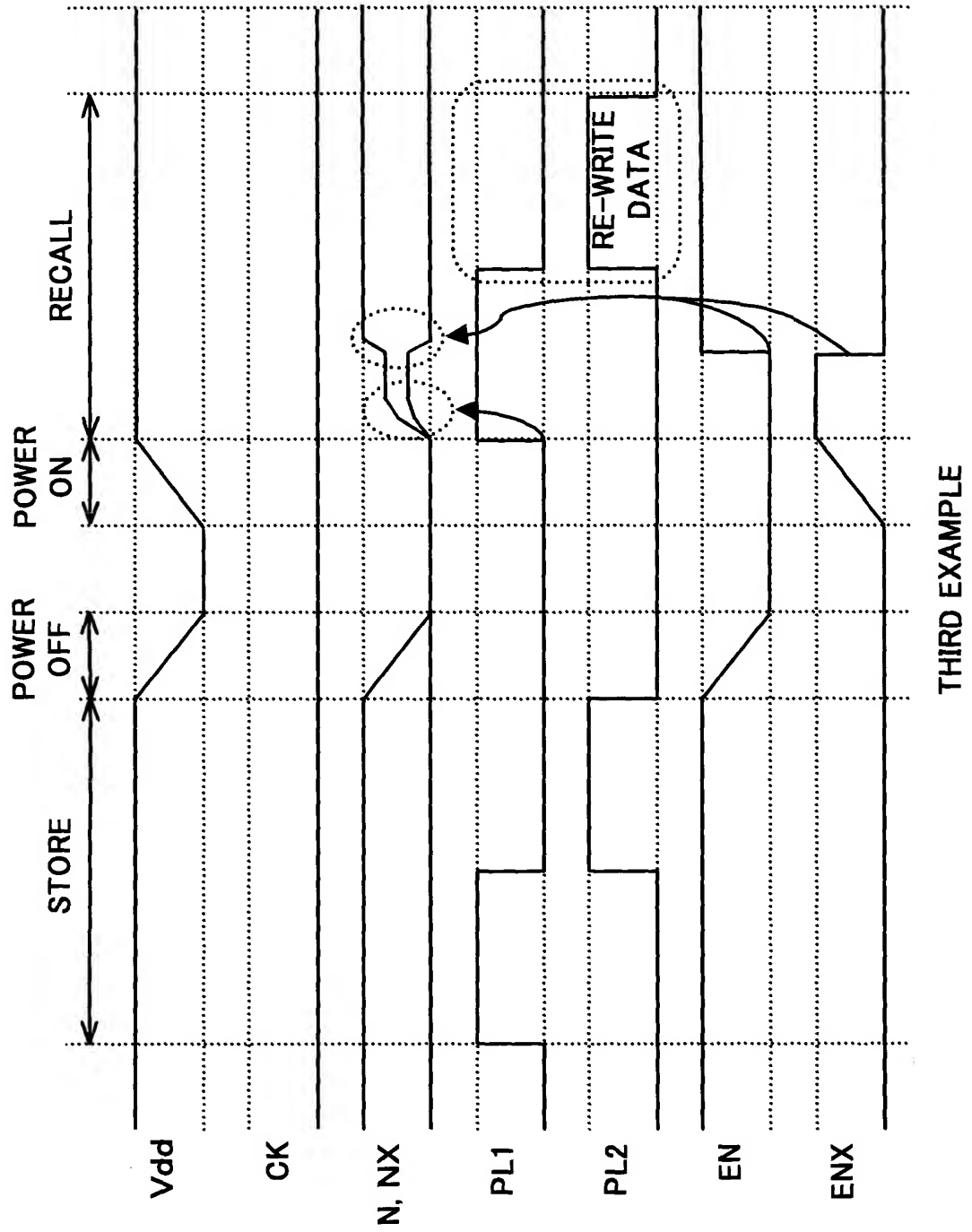


FIG. 15

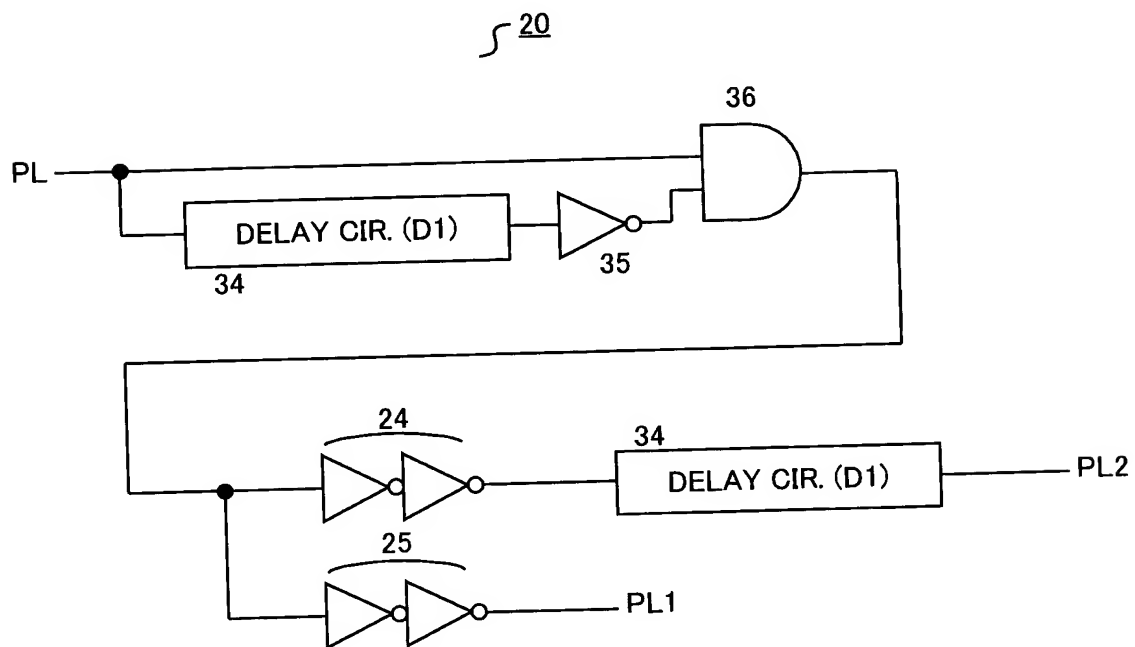


FIG. 16

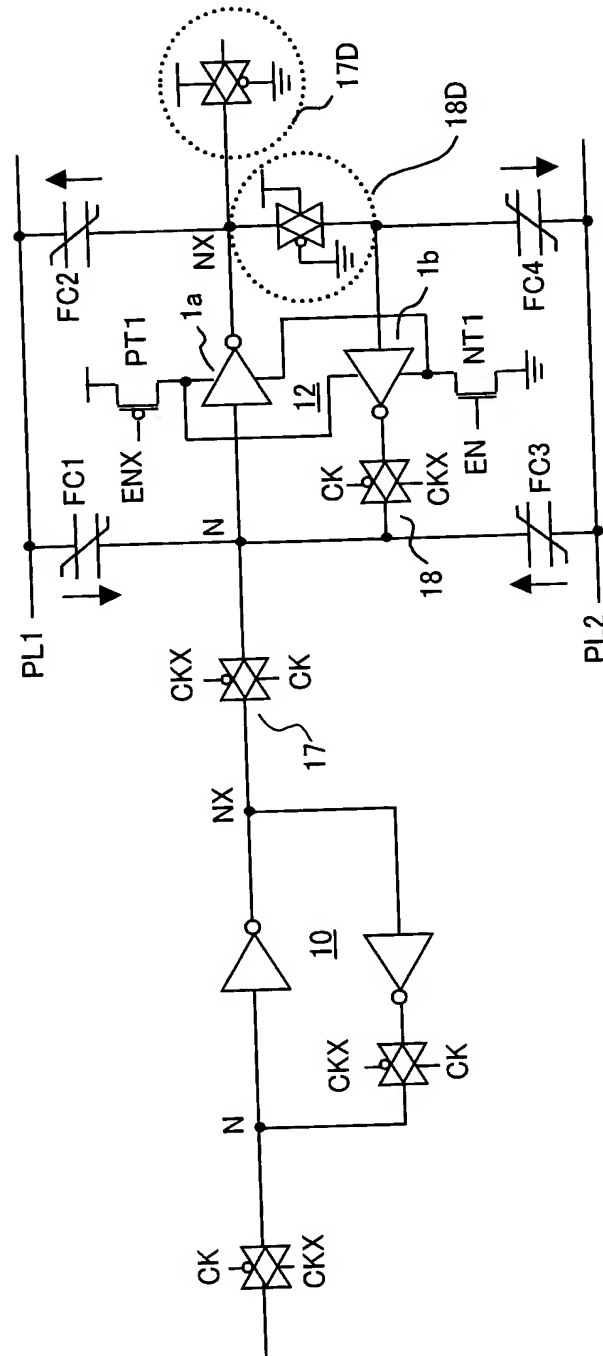


FIG. 17

MEMORY CIRCUIT EXAMPLE

